

LISTING OF CLAIMS

1. (currently amended) Apparatus comprising:
 - a network resource server having at least one computer system comprising a central processing unit and server memory; and
 - a network processor coupled to said network resource server comprising:
 - a plurality of interface processors;
 - instruction memory for storing instructions accessibly to said interface processors;
 - data memory for storing data passing through said network processor to and from said network resource server accessibly to said interface processors; and
 - a plurality of input/output ports;
 - one of said input/output ports adapted for exchanging data passing through said network processor with an external network under the direction of said interface processors;
 - at least one other of said input/output ports adapted for exchanging data passing through said network processor with said network resource server;
 - said network processor and said network resource server cooperating in directing the exchange of data between said input/output ports and the flow of data through said data memory to and from said network resource server in response to execution by said interface processors of instructions loaded into said instruction memory;
 - said network processor further comprising at least one rate monitor for monitoring the rate of data flow addressed
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from any source to said network resource server, at least one of said interface processors comprising a component for computing a derivative of data flow rate over time to determine the rate of change of data flow, and at least one modifier for modifying the instructions loaded into said instruction memory in response to the determined rate of change.

2. (original) Apparatus according to Claim 1 wherein said at least one interface processor further comprises means for comparing said determined rate of change to at least one predetermined boundary and wherein said modifier modifies the instruction when the rate of change has a prespecified relationship to said at least one predetermined boundary.
3. (original) Apparatus according to Claim 1 wherein said network processor comprises a semiconductor substrate and further wherein said interface processors, said instruction memory, said data memory and said input/output ports are formed on said semiconductor substrate.
4. (original) Apparatus according to Claim 1 wherein said network processor is adapted to process communication protocols and to exchange application data to be processed by said computer system with said network resource server.
5. (original) Apparatus according to Claim 4 wherein said network resource server has a plurality of computer systems serving diverse purposes and said network processor directs application data to an appropriate one of said computer

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systems for exercising the data in accordance with the appropriate purpose therefor.

6. (original) Apparatus according to Claim 4 wherein said network resource server has a plurality of computer systems serving a common purpose and said network processor directs application data to varying ones of said computer systems for exercising the data in a predetermined flow among said computer systems.

7. (original) Apparatus according to Claim 1 wherein the number of said interface processors exceeds four.

8. (original) Apparatus according to Claim 2 wherein said at least one rate monitor of said network processor monitors data flow inbound toward said network resource server and said at least one modifier increases the discard of inbound data upon the rate of change of inbound data exceeding a predetermined boundary.

9. (original) Apparatus according to Claim 2 wherein said at least one modifier of said network processor decreases the discard rate of inbound data upon the rate of change of inbound data falling below a second predetermined boundary.

10. (currently amended) A method in an apparatus comprising:
a network resource server comprising at least one computer system which has a central processing unit and server memory; and

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a network processor coupled to said network resource server and comprising:

- a plurality of interface processors;
- instruction memory for storing instructions accessibly to said interface processors;
- data memory for storing data passing through said network processor to and from said network resource server accessibly to said interface processors; and

- a plurality of input/output ports;

- one of said input/output ports adapted for exchanging data passing through said network processor with an external network under the direction of said interface processors;

- at least one other of said input/output ports adapted for exchanging data passing through said network processor with said network resource server;

said method comprising the steps of:

- said network processor cooperating with said network resource server in directing the exchange of data between said input/output ports and the flow of data through said data memory to and from said network resource server in response to execution by said interface processors of instructions loaded into said instruction memory;

- monitoring the rate of data flow inbound toward said network resource server from all data sources, and

- at least one of said interface processors computing a derivative of data flow rate over time to determine the rate of change of data flow from all data sources to said network resource server, and

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said at least one modifier modifying the instructions loaded into said instruction memory in response to the determined rate of change.

11. (previously presented) The method according to Claim 10 wherein said at least one interface processor further comprises means for comparing said determined rate of change to at least one predetermined boundary and wherein said modifier modifies the instruction when the rate of change has a prespecified relationship to said at least one predetermined boundary.

12. (previously presented) The method according to Claim 10 wherein said network processor comprises a semiconductor substrate and further wherein said interface processors, said instruction memory, said data memory and said input/output ports are formed on said semiconductor substrate.

13. (previously presented) The method according to Claim 10 wherein said network processor is adapted to process communication protocols and exchanges with said network resource server application data to be processed by said server.

14. (previously presented) The method according to Claim 12 wherein said network resource server comprises a plurality of computer systems serving diverse purposes and said network processor directs application data to the

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appropriate one of said systems for exercising the data in accordance with the appropriate purpose therefor.

15. (previously presented) The method according to Claim 12 wherein said network resource server comprises a plurality of computer systems serving a common purpose and said network processor directs application data to varying ones of said computer systems for exercising the data in a predetermined flow in said computer systems.

16. (previously presented) The method according to Claim 10 wherein the number of said interface processors exceeds four.

17. (previously presented) The method according to Claim 11 wherein said at least one rate monitor of said network processor monitors data flow inbound to said network resource server and said modifier decreases the discard of inbound data upon the rate of change of inbound data rising above a predetermined boundary.

18. (previously presented) The method according to Claim 11 wherein said modifier of said network processor decreases the discard rate of inbound data upon the rate of change of inbound data falling below above a second predetermined boundary.

19. (previously presented) A program storage device, readable by machine, tangibly embodying a program of instructions executable by the machine to, in an apparatus comprising a network resource server having at least one

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computer system comprising at least a central processing unit and server memory; and a network processor coupled to said network resource server comprising:

- a plurality of interface processors;
- instruction memory for storing instructions accessible to said interface processors;
- data memory for storing data passing through said network processor to and from said network resource server accessibly to said interface processors; and
- a plurality of input/output ports;

- one of said input/output ports adapted for exchanging data passing through said network processor with an external network under the direction of said interface processors;
 - at least one other of said input/output ports adapted for exchanging data passing through said network processor with said network resource server;

said method comprising the steps of:

- said network processor cooperating with said network resource server in directing the exchange of data between said input/output ports and the flow of data through said data memory to and from said network resource server in response to execution by said interface processors of instructions loaded into said instruction memory;

- said network processor monitoring the rate of data flow outbound from said network resource server, computing a derivative of data flow rate over time to determine the rate of change of data flow, and modifying the instructions

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loaded into said instruction memory in response to the determined rate of change.

20. (previously presented) The program storage device according to Claim 19 wherein said method further comprises comparing said determined rate of change to at least one predetermined boundary and modifying the instruction when the rate of change has a prespecified relationship to said at least one predetermined boundary.

21. (previously presented) The program storage device according to Claim 19 wherein said network processor comprises a semiconductor substrate and further wherein said interface processors, said instruction memory, said data memory and said input/output ports are formed on said semiconductor substrate.

22. (previously presented) The program storage device according to Claim 19 wherein said network processor is adapted to process communication protocols and exchange application data to be processed by said network resource server with said network resource server.

23. (previously presented) The program storage device according to Claim 22 wherein said network resource server has a plurality of computer systems serving diverse purposes and wherein said network processor directs application data to the appropriate one of said computer systems for exercising the data in accordance with the appropriate purpose therefor.

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24. (previously presented) The program storage device according to Claim 22 wherein said network resource server has a plurality of computer systems serving a common purpose and said network processor directs application data to varying ones of said computer systems for exercising the data in a predetermined flow among said computer systems.

25. (previously presented) The program storage device according to Claim 19 wherein the number of said interface processors exceeds four.

26. (previously presented) The program storage device according to Claim 20 wherein said monitoring step comprises monitoring data flow outbound from said network resource server and further comprising increasing the discard of inbound data upon the rate of change of outbound data falling below a predetermined boundary.

27. (previously presented) The program storage device according to Claim 20 further comprising decreasing the discard rate of inbound data upon the rate of change of outbound data rising above a second predetermined boundary.

28. (currently amended) A method comprising the steps of:
passing bit streams of data exchanged between a network resource server and a data network through a network processor;

monitoring the data flow rate of data passing through the network processor to and from all locations;

computing a first derivative of the data flow rate to determine the rate of change of the data flow rate; and

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selectively discarding data flowing toward the network resource server based upon said rate of change of the data flow rate.

29. (original) A method according to Claim 28 further comprising comparing said rate of change of the data flow rate to at least one predetermined boundary and wherein said selective discarding is conducted when said rate of change has a prespecified relationship to at least one predetermined boundary.

30. (original) A method according to Claim 28 wherein the step of monitoring comprises monitoring the rate of data flow inbound toward the network resource server.

31. (original) A method according to Claim 29 wherein the step of selectively discarding data comprises increasing the discard of data flowing toward the network resource server upon a determination that the rate of change of inbound data flow has risen above a predetermined boundary.

32. (original) A method according to Claim 29 wherein the step of selectively discarding data comprises decreasing the discard of data flowing toward the network resource server upon a determination that the rate of change of inbound data flow has fallen below a predetermined boundary.

33. (original) A method according to Claim 28 wherein the step of monitoring comprises monitoring the rate of data flow outbound from the network resource server.

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34. (original) A method according to Claim 33 wherein the step of selectively discarding data comprises increasing the discard of data flowing toward the network resource server upon a determination that the rate of change of outbound data flow has fallen below a predetermined boundary.

35. (original) A method according to Claim 33 wherein the step of selectively discarding data comprises decreasing the discard of data flowing toward the network resource server upon a determination that the rate of change of outbound data flow has risen above a predetermined boundary.

36. (currently amended) A network processor coupled to at least one network resource server and an external network comprising:

- a plurality of interface processors;
- instruction memory for storing instructions accessibly to said interface processors;
- data memory for storing data passing through said network processor to and from said network resource server accessibly to said interface processors; and
- a plurality of input/output ports;
 - one of said input/output ports adapted for exchanging data passing through said network processor with an external network under the direction of said interface processors;
 - at least one other of said input/output ports adapted for exchanging data passing through said network processor with said network resource server;

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said network processor and said network resource server cooperating in directing the exchange of data between said input/output ports and the flow of data through said data memory to and from said network resource server in response to execution by said interface processors of instructions loaded into said instruction memory;

said network processor further comprising at least one rate monitor for monitoring the rate of data flow addressed to said network resource server from all data sources, at least one of said interface processors comprising a component for computing a derivative of data flow rate over time to determine the rate of change of data flow, and at least one modifier for modifying the instructions loaded into said instruction memory in response to the determined rate of change.

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